



ABSTRACT

A video display driver circuit adapted to receive and monitor an input video signal, which is associated with information or an image that is subsequently displayed on a display. The video display driver circuit includes circuitry for detecting movement of the information or image displayed on the display and circuitry for adjusting a vertical synchronization frequency associated with the display to enable the moving information or image to be smoothly reproduced in a number of locations on the display.

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FOOTNOTES